



## **We make the world a smarter place**

Easics is the reference in smart electronic systems design. Our customers are world-leaders in their field. They give us interesting and rewarding challenges for first-of-a-kind applications. Take for example: neural net inference at the extreme edge, a radiation-hardened camera for earth observation satellites, top-notch hearing implants helping newborns and grandmas alike, power-harvesting wireless tags, ...

We discuss the complex algorithms and requirements with our customers and turn them into cutting-edge reality, solving the challenges inherent to silicon implementations. We model these challenges in software and optimize all layers of abstraction. Trade-offs in performance, power consumption and area are made along the way, resulting in the physical realization: an FPGA, a System-on-Chip or a custom-tailored ASIC at the heart of our customer's products.

Easics is based in Leuven - Belgium and is a spinoff of Imec and KU Leuven.

## **Design and Verification Engineering Intern Internship (4-6 months)**

We are currently looking for interns to assist us in projects including FPGA and digital ASICS design and verification. During your months at easics, we will give you an in-depth training for a subset of tasks our design engineers tackle every day. You will then apply this knowledge to real-life, cutting-edge hardware design.

You will be tasked with understanding and implementing or verifying modules that are part of larger designs. Depending on your speed of work, your responsibilities may include full ownership of modules. The languages and frameworks you will use are VHDL, (System)Verilog, C++, SystemC and a number of our own in-house hardware description languages.

### **Your Profile**

- You have demonstrable programming experience
- You have a basic understanding of synchronous digital circuitry design
  - You are fluent in English
  - You are a national of a country in the EEA

### **We offer**

- Real industry experience
- CoL reimbursement during your stay
- We organize housing during your stay